SERVICE DESCRIPTION

ATM Technical Security Review (Remote or Onsite)

Service Scope
The Trustwave SpiderLabs ATM Technical Security Review service blends multiple technical security assessment disciplines to provide holistic information assurance of an entire ATM network and its constituent ATM system devices.

Trustwave SpiderLabs are one of the few organizations globally to have investigated wholesale compromises of ATM networks. This experience of real-world ATM network compromises has been translated into a service that can be leveraged by banks and ATM network operators to identify security control weaknesses and failures of ATM machines and the underlying ATM network.

ATM Network Architecture Review
In order to assess the network layer security of the ATM network Trustwave SpiderLabs performs a review of the ATM network architecture to identify gaps in the security posture of the ATM network that could be utilized by an external attacker to bypass security controls or deploy customized, ATM specific malware.

Furthermore, SpiderLabs will perform a review of the development/lab environments used by the Client to build and test components of the ATM Network. The goal of this phase is to understand the risk of the internal threat from the employees working within this environment.

ATM Network Penetration Testing
Trustwave SpiderLabs performs an internal network penetration test of the ATM network; typically the testing location will be that an end point location, such as an ATM's network connection to the rest of the network. The objective of an internal network penetration test is to determine if the current network security controls are vulnerable to an attack from an attacker that has gained unauthorized access to the network either physically or virtually. This level of testing validates corporate security policy and development standards by attempting to identify how resilient the internal network is to determined attackers.

In addition, Trustwave will monitor the traffic traveling between the ATM and the banking network to help identify any useful/valuable data being sent in the clear. The goal of this portion of the engagement is to help identify network attack vector that exist against the target ATMs.
ATM Application Penetration Testing

If selected as part of this service, Trustwave SpiderLabs conducts application penetration testing of the software running on the ATM systems to identify application layer attack vectors that may be present on the ATM.

ATM Device Security Health Check

Trustwave SpiderLabs performs a review of one or more production ATM systems. This revolves around operating system security layer security checks. The operating system configuration of the ATM is reviewed for deviations from security best practice, specifically default configurations, default accounts, and default services that are enabled and that pose a risk to the overall security of the ATM.

In addition, specific security configuration settings, the security of third-party software, and logging and monitoring configuration settings are reviewed against security best practice.

The goal of this portion of the engagement is to review the operating system of the ATM to identify configuration issues that pose a risk of compromise by malware/attacker.

Finally, Trustwave SpiderLabs performs physical security testing of the ATM system, including the ATM housing enclosure, the ATM computer’s connections ports, the ATMs physical security locks and of the cash cassettes.

During this portion of the engagement, Trustwave attempts to develop methods to circumvent the physical controls in place on the ATM. This includes lock picking and using tools to attempt to gain access to the internal workings of the ATM while mitigating any physical damage to the ATM itself. The goal of this portion of the engagement is to attempt to identify physical attack vectors that exist against the target ATM(s).

Supplementary Services

Network Penetration Test (Internal/External)  [CORE]
Application Penetration Test  [OPTIONAL]
Physical Penetration Test  [CORE]